

AM The presently disclosed embodiments are therefore to be considered in all respects as illustrative and not restrictive; the scope of the embodiments of the invention being indicated by the appended claims, rather than the foregoing description. All changes that come within the meaning and range of equivalency of the claims are therefore intended to be embraced therein.

IN THE CLAIMS:

Please amend claims 12 through 22 as follows:

12. (Amended) An article comprising:

a storage medium having stored thereon instructions that when executed by a machine result in the following:

AS
cont. sending a data packet over a data communication network to a receiver protocol state machine that stores the data packet in an application buffer;

waiting for an acknowledgment of receipt of the data packet from the receiver protocol state machine; and

arranging for a transmission of additional data packets.

13. (Amended) The article of claim 12, wherein instructions are provided to a transmitter protocol state machine to send the data packet while in a high power, high clock rate mode.

14. (Amended) The article of claim 13, wherein instructions are provided to the transmitter protocol state machine to enter an idle low power, low clock rate mode upon completion of data packet transmission.

15. (Amended) The article of claim 14, wherein instructions are provided to the transmitter protocol state machine to return to a high power, high clock rate mode upon a sounding of a timer.

16. (Amended) The article of claim 12, wherein an application buffer and a timer cause periodic patterns in data packet transmission, which are used to manage power and frequency of a processor.

AS
cont.
17. (Amended) An article comprising:
a storage medium having stored thereon instructions that when executed by a machine result in the following:

receiving a data packet from a transmitter protocol state machine over a data communication network;

depositing the data packet in an application buffer;

processing and verifying the data packet; and

transmitting an acknowledgment of receipt of the data packet to the transmitter protocol state machine, wherein the transmitter protocol state machine prepares for transmission of additional data packets.

18. (Amended) The article of claim 17, wherein instructions are provided to a receiver protocol state machine to obtain delivery of the data packet, to store the data packet in

the application buffer, to process the data packet, and to send the acknowledgment of receipt of the data packet.

19. (Amended) The article of claim 18, wherein instructions are provided to the receiver protocol state machine to enter an idle low power, low clock rate mode upon obtaining delivery of the data packet.

20. (Amended) The article of claim 19, wherein instructions are provided to the receiver protocol state machine to enter a high power, high clock rate mode when the application buffer reaches a maximum capacity.

21. (Amended) The article of claim 17, wherein the application buffer and a timer cause periodic patterns in data packet reception, which are used to manage power and frequency of a processor.

22. (Amended) The article of claim 17, wherein the data communication network includes at least one of the Internet and an Intranet.
